

<b>Notice of References Cited</b>	Application/Control No. 10/713,943		Applicant(s)/Patent Under Reexamination KIM ET AL.	
	Examiner Kaushikkumar Patel		Art Unit 2188	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,623,654 A	04-1997	Peterman, Jim	707/206
*	B	US-5,784,699 A	07-1998	McMahon et al.	711/171
*	C	US-6,430,665 B1	08-2002	Allison et al.	711/172
*	D	US-2002/0144073 A1	10-2002	Trainin et al.	711/170
*	E	US-2003/0018689 A1	01-2003	Ramakrishnan, Balakrishnan	709/104
*	F	US-2003/0084266 A1	05-2003	Knippel et al.	711/173
*	G	US-2003/0121030 A1	06-2003	Koob et al.	717/152
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Hardware Support for Fast and Bounded-Time Storage Allocation, by Donahue et al. (published March 22, 2002). Pages 10.			
	V	Linked List Basics by Nick Parlante. 2001. Pages 26.			
	W	Datastructure and Algorithms, Lists by John Morris 1998. Pages 3. ( <a href="http://www.oopweb.com/algorithms/documents/PLDS210/volume/lists.html">http://www.oopweb.com/algorithms/documents/PLDS210/volume/lists.html</a> )			
	X	Principles of Computer Architecture by, Miles J. Murdicca (pub. 2000). Page 270.			

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.